

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1898	(disc same baseplate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:27
L2	1	(disc same baseplate same pcb same (servo or controller) same cpu)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:10
L3	1	(baseplate same pcb same (servo with controller) same cpu)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:10
L4	2	(pcb same (servo with controller) same cpu)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:11
L5	2	(pcb same (servo same controller) same cpu)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:12
L6	27	((printed with circuit with board) same (servo same controller) same cpu)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:28
L7	27	((printed with circuit with board) same (servo with controller) same cpu)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:14
L8	21	((printed with circuit with board) same (servo with controller) same cpu same disc)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:14
L9	14	((printed with circuit with board) same (servo with controller) same cpu same disc) and @ad<"20010821"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:16

L10	1	((printed with circuit with board) same (servo with controller) same cpu same disc) and @ad<"20000823"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:31
L11	1	((((printed with circuit with board) or pcb) same (servo with controller) same cpu same disc) and @ad<"20000823"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:31
L12	1	((((printed with circuit with board) or pcb) same (servo with controller) same cpu same rot\$5) and @ad<"20000823"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:43
L13	1	((((printed with circuit with board) or pcb) same ((servo with controller) or servocontroller or servo-controller) same cpu same rot\$5) and @ad<"20000823"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:43
L14	5	((((pcb or printed with circuit with board)) same ((servo with controller) or servocontroller or servo-controller) same cpu same stor\$5) and @ad<"20000823"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 16:44
L15	25	bruner.in. and pcb	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:28
L16	0	bruner.in. and pcb and harmer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 17:45
L17	24	bruner.in. and harmer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 17:45
L18	24	bruner.in. and harmer.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 17:45
L19	23	bruner.in. and harmer.in. and @ad<"20000823"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 17:45

L20	8	(disc same baseplate) and wood.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 17:47
L21	84	(disc same baseplate) and seagate.as.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:00
L28	0	(disc same baseplate) and (711/112.ccls. or 711/113.ccls. or 711/202.ccls. or 395/500.ccls. or 395/280.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:27
L29	2	(disc same baseplate) and (711/112.ccls. or 711/113.ccls. or 711/202.ccls. or 395/500.ccls. or 395/280.ccls. or "711"/\$.ccls. or "710"/\$.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:27
L30	8	((printed with circuit with board) same (servo same controller) same cpu) and (711/112.ccls. or 711/113.ccls. or 711/202.ccls. or 395/500.ccls. or 395/280.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:28
L31	5	bruner.in. and pcb and (711/112.ccls. or 711/113.ccls. or 711/202.ccls. or 395/500.ccls. or 395/280.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:28
L32	0	wood.in. and pcb and (711/112.ccls. or 711/113.ccls. or 711/202.ccls. or 395/500.ccls. or 395/280.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:28
L33	0	wood.in. and pcb.clm. and (711/112.ccls. or 711/113.ccls. or 711/202.ccls. or 395/500.ccls. or 395/280.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:28
L34	6	wood.in. and pcb.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:28
L35	1	wood.in. and (pcb and cpu).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:28

L36	1	wood.in. and (pcb and servo).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/13 18:28
-----	---	-----------------------------------	---	----	----	------------------